AK

Application No. 09/939,498 Examiner	Applicant(s)	
	CUDDANAANIAN ET AL	
Examiner	SUBRAMANIAN ET AL.	
	Art Unit	
DAVID VU	2818	
S (OR REMAINS) CLOSED in 5) or other appropriate community RIGHTS. This application is self-and MPEP 1308.  under 35 U.S.C. § 119(a)-(d) over been received.	n this application. If not included unication will be mailed in due course. The subject to withdrawal from issue at the initial part of the initia	
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IMENT of this application.  mitted. Note the attached EXA	AMINER'S AMENDMENT or NOTICE OF	
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6. Interview So Paper No./ 3/08), 7. Examiner's	ummary (PTO-413), Mail Date	
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## **DETAILED ACTION**

## **Election/Restrictions**

1. Applicant's election without traverse of Species I (Claims 16-22), in the reply filed on 10/11/2005, is acknowledged.

- 2. This application is in condition for allowance except for the presence of claims 107-115 and 122-125 non-elected without traverse. Accordingly, claims 107-115 and 122-125 have been cancelled.
- 3. Cancel claims 107-115 and 122-125.

## Allowable Subject Matter

- 4. Claims 95-100 and 120-121 are allowed.
- 5. The following is an examiner's statement of reason for allowance: the prior art of record, either singularly or in combination, does not disclose or suggest a process for fabricating a single element antifuse in a 3-D semiconductor memory device comprising: forming a semiconductor layer; wherein the semiconductor layer comprises polycrystalline semiconductor material; oxidizing at least a portion of the semiconductor layer in a plasma to form an oxide antifuse layer overlying the semiconductor layer, as instantly claimed and in combination with the additionally claimed method steps.

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Any comments considered necessary by applicant must be submitted no later than the

payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for

Allowance".

Conclusion

6. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to David Vu whose telephone number is 571-272-1798. The

examiner can normally be reached on Monday-Friday 8:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, David Nelms can be reached on 571-272-1787. The fax phone numbers for the

organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR, Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

David Vu

**Primary Examiner**